

GPUs as a possible L1 Track Trigger

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March 7, 2016

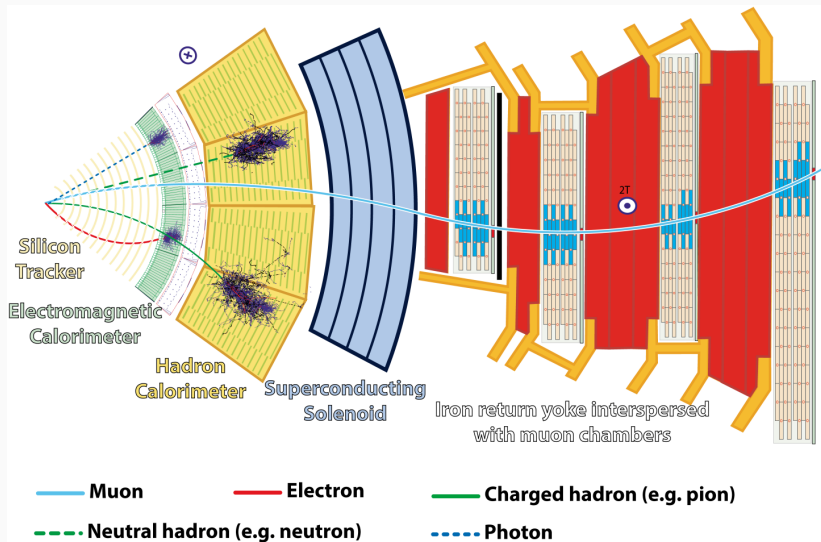
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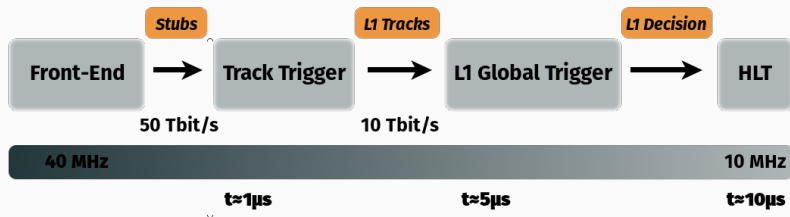
- **High Luminosity-LHC**: Upgrade of the LHC (≈ 2025)
- Observations of events below current sensitivity level
- Current Luminosity increased by a **factor of 10**
- ≈ 12.000 Hits per Bunch crossing in the outer Tracker
- Bunch crossings happen every **25 ns**

\Rightarrow Complete redesign of the Tracker and the Trigger System

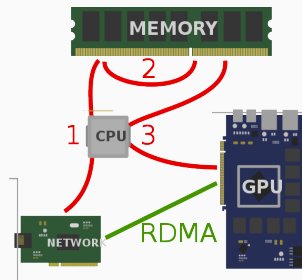
CMS Detector Layers, Transverse Slice



Experimental Requirements



- Low Latency
- High Frequency
- High Data Rates

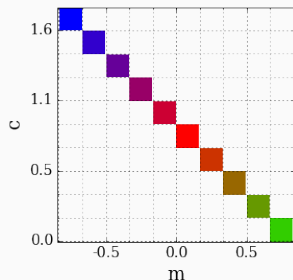
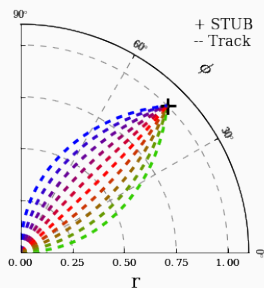


Hough Transformation

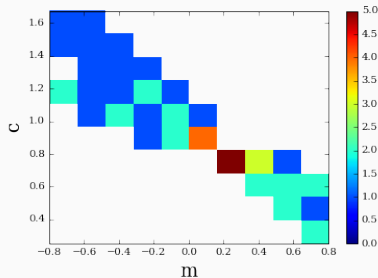
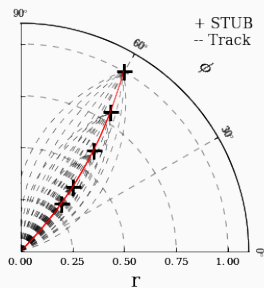
Transformation from Normal Space to Houghspace

$$\phi(r) = mr + c$$

Each possible track for a stub, leads to a point in Hough Space

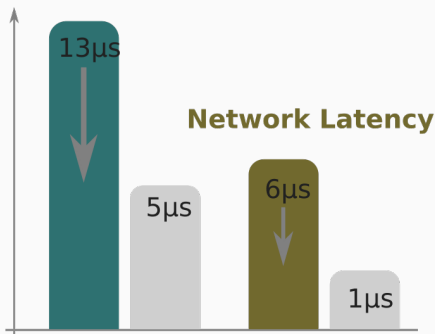


Multiple stubs from the same **track**, lead to intersecting lines

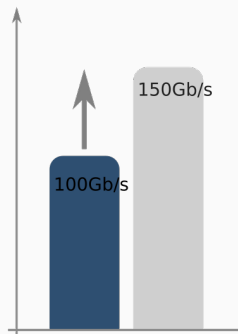


More than 5 hits in one **cell**, lead to a readout of this cell's stubs

Algorithm runtime



Interconnect Throughput



(Grey) Required performance

Preliminary Benchmark¹

¹Processing time for 1 ϕ -sector, containing 500 stubs, assuming 36 ϕ -sectors in total, benchmarking done on a TitanX, Using CUDA 7.5

Questions?

Stub Principle

