Topical Workshop: FPGAs in Research - Applications, Technologies and Tools

Tuesday, December 4, 2012

Talks TUE1 - Room 110 in Building 2.5 (11:00 AM - 12:15 PM)

time	[id] title	presenter
11:00 AM	[17] FPGA Programming Methods - An Overview	GOEHRINGER, Diana
11:25 AM	[11] Matlab/Simulink and HDL-coder	Mr XIONG, Renhai
11:50 AM	[15] Parallelisation potential of image segmentation in hierarchical island structures on hardware-accelerated platforms in real-time applications	Mr SUSLOV, Sergey