



Contribution ID : 159

Type : **Talk**

Microchips for Megatrends – Work where tomorrow is made

Saturday, 26 November 2022 11:00 (20)

In this talk, I would like to outline my daily tasks at my job at ZEISS Semiconductor Manufacturing Technology and my academic path to arriving here. I completed degrees in physics in several countries: Bachelor's degree from the University of Luxembourg, Master's degree from Joseph Fourier University in Grenoble and a PhD from the Aix-Marseille University. Since finishing my PhD four years ago, I have been working at ZEISS Semiconductor Manufacturing Technology as a research assistant. My day-to-day work cannot be described in a single sentence as it varies and is challenging. It extends from preparing two 40T vacuum chambers for our high-end processes, through to testing our processes on new materials to project planning for the next fiscal year. Slowly but surely, my colleagues and I are contributing every day to creating even small and faster semiconductors – until we reach the limits of Moore's law?

Category

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Session Classification : Physics and Profession 3

Track Classification : Physics and profession