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Real-Time Studies of Resistive Switching Mechanisms

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Exponential increase in the demand for data storage and processing

A promising solution: **Resistive switching memory**

Real-time biasing experiments in the TEM







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- Volume of data/information created, captured, copied, and consumed worldwide from 2010 to 2020, with forecasts from 2021 to 2025 (in zettabytes).
- Changes in computing architecture and hardware are therefore urgently needed to meet the increased demands for data storage and processing.

• In situ biasing setup in the TEM is a to unravel resistive great tool switching mechanisms in different material systems.

Filamentary and interface-type switching (Valence change material, VCM)

Device fabrication on MEMS chip



- Micro-crossbar device on Si_3N_4 • Metal oxide layers
- grown by ALD
- TiO₂ switching layer

Phase change switching (Phase change material, PCM)

membrane

Device fabrication on MEMS chip



40 nm SiO₂-ZnS cap 40 nm AIST device

150 nm Au contacts

380 µm Si wafer

- Ag-In-Sb-Te (AIST) cells (bridge structure) on Si_3N_4 membrane
- Capping layer for the heat dissipation

• AIST switching layer



- Contrast changes at the bridge observed by HAADF-STEM
- Need to confirm whether contrast changes originate from the transition between amorphous and crystalline phases of AIST or not

Identification of amorphous and crystal by 4D-STEM

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- Filamentary switching at higher voltage and compliance current in the same device
- Non-negative matrix factorization (NMF) performed to extract feature spectra
- Local formation of conducting filament (reduction of Ti and oxidation of Cr) [5]

Conclusions

- Reliable resistive switching of PCM and VCM was successfully investigated by in *situ* electrical biasing setup with advanced TEM techniques.
- Correlative measurements by TXM and TEM revealed the presence of a conducting filament, which is the origin of the filamentary switching of VCM device.
- 4D-STEM with a nanobeam electron diffraction enabled tracking the annihilation and recreation of conduction path (e.g. evolution of crystalline grains) of AIST during resistive switching.



Color code for individual crystals

- Differentiation between amorphous and crystalline phases using 4D-STEM
- Rupture and recreation of the conduction path caused by the transition between amorphous and crystalline phases, resulting in the evolution of crystalline grains

References

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